

new/usr/src/uts/i86pc/vm/hat\_i86.c

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5601 TLB invalidation regression in 5498

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\_\_\_\_\_ unchanged\_portion\_omitted\_

2165 void

2166 hat\_tlb\_inval(hat\_t \*hat, uintptr\_t va)

2167 {

2168         hat\_tlb\_inval\_range(hat, va, 1);

2168         hat\_tlb\_inval\_range(hat, va, MMU\_PAGESIZE);

2169 }

\_\_\_\_\_ unchanged\_portion\_omitted\_